



Renesas Electronics is a leading provider of integrated circuits, for a wide range of applications, including smartphone, automotive, computing & storage and the IoT.

As part of our continued growth, we are looking for:

Junior Digital IC Design Engineer.

Company: [Renesas Electronics](#)

Location: Lviv, Kamyanetska, 33 str

The role:

- Master's or Ph.D. in Electrical Engineering or related disciplines;
- Preferably some experience in design of analog/digital circuits (**0–1 years of experience**);
- Knowledge of Xcelium,(System)Verilog/VHDL/scripting languages;
- Full understanding of IC design flow;
- Self-motivated and take full responsibility for solutions;
- Good written, communication and teamwork skills;
- (Pre-)intermediate English level.

Responsibilities:

- Participate in feasibility studies and sub-block/chip-level architecture definition;
- Work closely with back-end designers to correctly implement digital designs to layout;
- Verification planning, verification methodology and test bench development using Verilog/VHDL;
- Static timing analysis (STA), formal verification, logic equivalence checking;
- Support production test and HW/SW development for evaluation, characterization and testing;
- Attendance at and participation in all relevant project meetings, keeping project manager aware of achievements, issues and potential risks to timescales;
- Demonstrates an ability to learn new tool features and procedures;
- Attend training courses or conferences.

We offer:

- Friendly and highly professional team;
- 28 calendar days paid vacation;
- Medical insurance;
- English classes;
- Flexible working hours;
- Professional & personal growth.

Send your CV to oksana.khrypko.ry@renesas.com or apply here jobs.dou.ua